



इटर-इण्डिया, प्लाज़्मा अनुसंधान संस्थान

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CORRIGENDUM-1 DATED 22.10.2021

Tender No. I-ITN21002 Dated 23rd September 2021

**For Development of FPGA (Field Programmable Gate Arrays) based RF Control System for RF Source
(TWO PART PUBLIC TENDER)**

It is hereby notified to the bidders that, the due date for submission of the bid against our **Tender No. I-ITN21002 dated 23.09.2021** for "Development of FPGA (Field Programmable Gate Arrays) based RF Control System for RF Source" (Two Part Public Tender) is **extended up to 15-November-2021 by 1:00 P.M. (IST)**. **PART-A of the bid will be opened on the same date at 2:30 P.M. (IST)**.

Except this corrigendum, all other Essential Eligibility Criteria, Scope of Supply, Work, Technical Specifications and Terms & Conditions of the above mentioned tender shall remain unchanged.